### 2.0A SCHOTTKY BARRIER DIODE

### **Features**

- Schottky Barrier Chip
- Guard Ring for Transient and ESD Protection
- Surge Overload Rating to 50A Peak
- Low Power Loss, High Efficiency
- Ideally Suited for Use in High Frequency SMPS, Inverters and As Free Wheeling Diodes

## **Mechanical Data**

Case: DO-15, Molded Plastic

Terminals: Plated Leads Solderable per

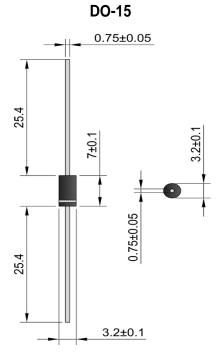
MIL-STD-202, Method 208

Polarity: Cathode Band

Weight: 0.40 grams (approx.)

Mounting Position: AnyMarking: Type Number

Lead Free: For RoHS / Lead Free Version



# Maximum Ratings and Electrical Characteristics @TA=25°C unless otherwise specified

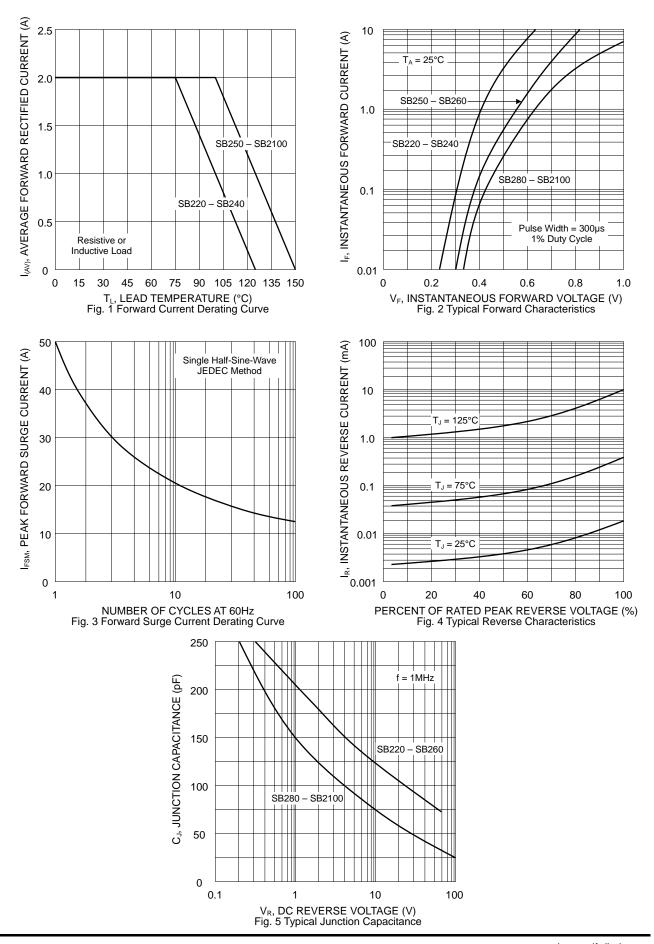
Single Phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.

| Characteristic  | Symbol             | SB220                   | SB230 | SB240 | SB250 | SB260 | SB280 | SB2100 | Unit |
|---|--------------------|-------------------------|-------|-------|-------|-------|-------|--------|------|
| Peak Repetitive Reverse Voltage<br>Working Peak Reverse Voltage<br>DC Blocking Voltage                                | Vrrm<br>Vrwm<br>Vr | 20                      | 30    | 40    | 50    | 60    | 80    | 100    | V    |
| RMS Reverse Voltage   | VR(RMS)            | 14                      | 21    | 28    | 35    | 42    | 56    | 70     | V    |
| Average Rectified Output Current (Note 1)   | lo                 | 2.0                     |       |       |       |       |       |        | Α    |
| Non-Repetitive Peak Forward Surge Current<br>8.3ms Single Half Sine-Wave Superimposed on<br>Rated Load (JEDEC Method) | IFSM               | 50                      |       |       |       |       |       |        | А    |
| Forward Voltage @I <sub>F</sub> = 2.0A  | VFM                | 0.50 0.70               |       |       |       | 70    | 0.85  |        | V    |
|   | IRM                | 0.5<br>10               |       |       |       |       |       | mA     |      |
| Typical Junction Capacitance (Note 2)   | Сл                 | 150                     |       |       |       |       | 100   |        | pF   |
| Thermal Resistance, Junction to Ambient (Note 3)<br>Thermal Resistance, Junction to Lead (Note 3)                     | R JA<br>R JL       | 35<br>14                |       |       |       |       |       |        | °C/W |
| Operating Temperature Range   | TJ                 | -65 to +125 -65 to +150 |       |       |       |       |       | °C     |      |
| Storage Temperature Range   | Тѕтс               | -65 to +150             |       |       |       |       |       |        | °C   |

Note: 1. Leads maintained at ambient temperature at a distance of 9.5mm from the case.

- 2. Measured at 1.0 MHz and applied reverse voltage of 4.0V D.C.
- 3. Vertical PCB mounting with 12.7mm lead length on 63.5 x 63.5mm copper pad.

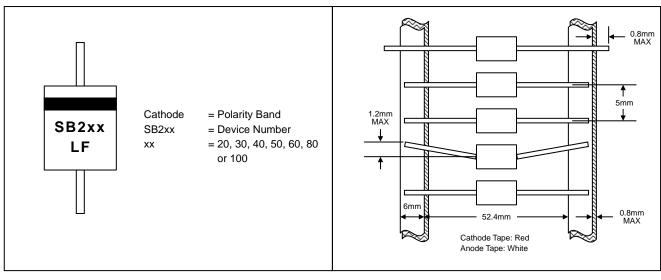






## **MARKING INFORMATION**

### **TAPING SPECIFICATIONS**



### PACKAGING INFORMATION

