5.0A SCHOTTKY BARRIER DIODE

Features

- Schottky Barrier Chip
- Guard Ring for Transient and ESD Protection
- Surge Overload Rating to 150A Peak
- Low Power Loss, High Efficiency
- Ideally Suited for Use in High Frequency SMPS, Inverters and As Free Wheeling Diodes

Mechanical Data

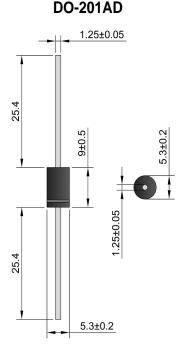
Case: DO-201AD, Molded Plastic
Target and Plast Alexander

 Terminals: Plated Leads Solderable per MIL-STD-202, Method 208

Polarity: Cathode BandWeight: 1.2 grams (approx.)

Mounting Position: AnyMarking: Type Number

Lead Free: For RoHS / Lead Free Version



Maximum Ratings and Electrical Characteristics @TA=25°C unless otherwise specified

Single Phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.

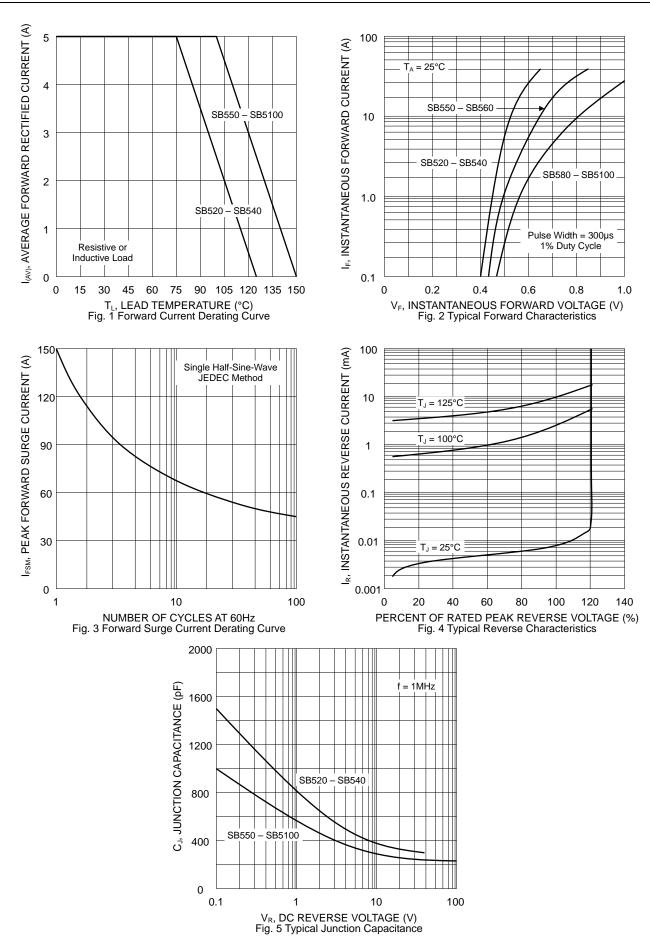
Characteristic	Symbol	SB520	SB530	SB540	SB550	SB560	SB580	SB5100	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	VRRM VRWM VR	20	30	40	50	60	80	100	V
RMS Reverse Voltage	VR(RMS)	14	21	28	35	42	56	70	V
Average Rectified Output Current (Note 1)	lo	5.0							Α
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load (JEDEC Method)	IFSM	150							А
Forward Voltage @I _F = 5.0A	VFM	0.55 0.70 0.85					85	V	
Peak Reverse Current $@T_J = 25^{\circ}C$ At Rated DC Blocking Voltage $@T_J = 100^{\circ}C$	IRM	0.5 20							mA
Typical Junction Capacitance (Note 2)	CJ	500 380					pF		
Thermal Resistance, Junction to Ambient (Note 3) Thermal Resistance, Junction to Lead (Note 3)	R JA R JL	25 8.0							°C/W
Operating Temperature Range	TJ	-65 to +125 -65 to +150						°C	
Storage Temperature Range	Тѕтс	-65 to +150							°C

Note: 1. Leads maintained at ambient temperature at a distance of 9.5mm from the case.

- 2. Measured at 1.0 MHz and applied reverse voltage of 4.0V D.C.
- 3. Vertical PCB mounting with 12.7mm lead length on 63.5 x 63.5mm copper pad.



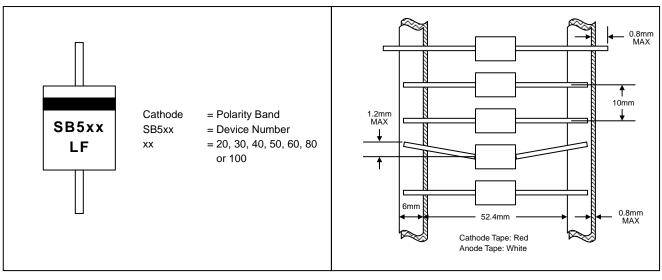






MARKING INFORMATION

TAPING SPECIFICATIONS



PACKAGING INFORMATION

